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☐　**1. A segmented gray code for low-power microcontroller address buses**
Hakenes, R.; Manoli, Y.;
EUROMICRO Conference, 1999. Proceedings. 25th
Volume 1, 8-10 Sept. 1999 Page(s):240 - 243 vol.1
Digital Object Identifier 10.1109/EURMIC.1999.794474

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☐　**2. Improving microcontroller power consumption through a segmented gray code program counter**
Hakenes, R.; Manoli, Y.;
Computer Design, 1999. (ICCD '99) International Conference on
10-13 Oct. 1999 Page(s):277 - 278
Digital Object Identifier 10.1109/ICCD.1999.808437

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☐　**3. Some issues in gray code addressing**
Mehta, H.; Owens, R.M.; Irwin, M.J.;
VLSI, 1996. Proceedings., Sixth Great Lakes Symposium on
22-23 March 1996 Page(s):178 - 181
Digital Object Identifier 10.1109/GLSV.1996.497616

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☐　**4. A BiCMOS thermal head intelligent driver with density controllers for full-tone rendition printers**
Tsumura, M.; Takeuchi, R.; Shimizu, I.;
Solid-State Circuits, IEEE Journal of
Volume 23, Issue 2, April 1988 Page(s):437 - 441
Digital Object Identifier 10.1109/4.1004

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☐　**5. Improved binarization algorithm for document image by histogram and edge detection**
Moon-Soo Chang; Sun-Mee Kang; Woo-Sik Rho; Heok-Gu Kim; Duck-Jin Kim;
Document Analysis and Recognition, 1995., Proceedings of the Third International Conference on
Volume 2, 14-16 Aug. 1995 Page(s):636 - 639 vol.2
Digital Object Identifier 10.1109/ICDAR.1995.601976

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☐　**6. A monolithic CMOS 16 channel, 12 bit, 10 microsecond analog to digital converter integrated circuit**
Milgrome, O.B.; Kleinfelder, S.A.;
Nuclear Science Symposium and Medical Imaging Conference, 1992., Conference Record of the 1992 IEEE

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7. **A monolithic CMOS 16 channel, 12 bit, 10 microsecond analog to digital converter integrated circuit**
Milgrome, O.B.; Kleinfelder, S.A.;
Nuclear Science, IEEE Transactions on

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8. **A low power fault secure timer implementation based on the Gray encoding scheme**
Papadomanolakis, K.S.; Kakarountas, A.P.; Sklavos, N.; Goutis, C.E.;
Electronics, Circuits and Systems, 2002, 9th International Conference on

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9. **Oblivious fragile watermarking scheme for image authentication**
Chang-Tsun Li; Fong-Man Yang; Chang-Shiu Lee;
Acoustics, Speech, and Signal Processing, 2002. Proceedings. (ICASSP '02). IEEE International Conference on

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10. **Trading off reliability and power-consumption in ultra-low power systems**
Maheshwari, A.; Burleson, W.; Tessier, R.;
Quality Electronic Design, 2002. Proceedings. International Symposium on

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11. **Low power dissipation in BIST schemes for modified Booth multipliers**
Bakalis, D.; Vergos, H.T.; Nikolos, D.; Kavousianos, X.; Alexiou, G.Ph.;
Defect and Fault Tolerance in VLSI Systems, 1999. DFT '99. International Symposium on

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12. **A High-Resolution Time-to-Digital Converter Implemented in Field-Programmable-Gate-Arrays**
Song, J.; An, Q.; Liu, S.;
Nuclear Science, IEEE Transactions on

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Majithia, J.C.; Kerstan, E.;
Proceedings of the IEEE

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Shibata, N.; Watanabe, M.; Tanabe, Y.;
Solid-State Circuits, IEEE Journal of

Volume 37, Issue 6, June 2002 Page(s):735 - 750
Digital Object Identifier 10.1109/JSSC.2002.1004578

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15. **Frequency band characteristics of tree-structured filter banks**
Nezih Gerek, O.; Nafi Gurcan, M.; Enis Cetin, A.;
Electronics Letters
Volume 32, Issue 8, 11 April 1996 Page(s):724 - 726

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16. **A novel low-power microprocessor architecture**
Hakenes, R.; Manoli, Y.;
Computer Design, 2000. Proceedings. 2000 International Conference on
17-20 Sept. 2000 Page(s):141 - 146
Digital Object Identifier 10.1109/ICCD.2000.878280

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17. **Clustering by SOM (self-organising maps), MST (minimal spanning tree) and MCP (modified counter-propagation)**
Obu-Cann, K.; Iwamoto, K.; Tokutaka, H.; Fujimura, K.;
Neural Information Processing, 1999. Proceedings. ICONIP '99. 6th International Conference on
Volume 3, 16-20 Nov. 1999 Page(s):986 - 991 vol.3
Digital Object Identifier 10.1109/ICONIP.1999.844670

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18. **Logic synthesis**
Hollingworth, P.;
Euro ASIC '90
29 May-1 June 1990 Page(s):250 - 256
Digital Object Identifier 10.1109/EASIC.1990.207949

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19. **Image segmentation based on consensus voting**
Shih-Hung Chen; Ming-Jui Kuo; Jung-Hua Wang;
Cellular Neural Networks and Their Applications, 2005 9th International Workshop on
28-30 May 2005 Page(s):1 - 4
Digital Object Identifier 10.1109/CNNA.2005.1543146

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20. **Validation of synthetic diamond for a beam condition monitor for the compact muon solenoid experiment**
Chong, D.; Fernandez-Hernando, L.; Gray, R.; Ilgner, C.J.; Macpherson, A.L.; Oh, A.; Pritchard, T.W.; Stone, R.; Worm, S.;
Nuclear Science Symposium Conference Record, 2004 IEEE
Volume 3, 16-22 Oct. 2004 Page(s):1812 - 1815 Vol. 3
Digital Object Identifier 10.1109/NSSMIC.2004.1462595

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21. **Turbo coding in ADSL DMT systems**
Zhang, L.; Yongacoglu, A.;
Communications, 2001. ICC 2001. IEEE International Conference on
Volume 1, 11-14 June 2001 Page(s):151 - 155 vol.1
Digital Object Identifier 10.1109/ICC.2001.936292

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22. **Motion detection and velocity estimation using cellular automata**
Kouzani, A.Z.; Bouzerdoum, A.; Liebelt, M.;
Intelligent Information Systems,1994. Proceedings of the 1994 Second Australian and New Zealand Conference on

29 Nov.-2 Dec. 1994 Page(s):327 - 331
Digital Object Identifier 10.1109/ANZIIS.1994.396982

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23. **Device and Circuit Level Optimization for High Performance a-Si:H TFT-Based AMOLED Displays**
Sambandan, S.; Striakhilev, D.; Nathan, A.;
Display Technology, Journal of
Volume 2, Issue 1, March 2006 Page(s):52 - 59
Digital Object Identifier 10.1109/JDT.2005.863600

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24. **Frequency-Counted Measurements and Phase Locking to Noisy Oscillators**
Gray, R.; Tausworthe, R.;
Communications, IEEE Transactions on [legacy, pre - 1988]
Volume 19, Issue 1, Feb 1971 Page(s):21 - 30

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25. **32-channel digital 6-bit TDC with 2.5 ns least count**
Cousins, R.D.; Friedman, C.; Melese, P.L.;
Nuclear Science, IEEE Transactions on
Volume 36, Issue 1, Part 1, Feb. 1989 Page(s):646 - 649
Digital Object Identifier 10.1109/23.34518

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are when one of the bits in the **grey counter** changes and the ... and shows a strong
repetitive **structure**. This is a typical. MTD133 result. ...
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20' wide custom backwall **structure** with sidewalls (material choices include: white or black
laminate or **gray** or blue fabric); shelf on the backwall header ...
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A **counter** is a state machine that conforms. to this **structure**. ... Figure 5 shows a **Gray**
code **counter** as a state machine design. The conven- ...
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Namespace **Gray** contains a local using directive for namespace Black, ...
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10617494	Not Issued	30	07/11/2003	Scalable gray code counter	CLIFT, GRAHAM
10967861	Not Issued	30	10/18/2004	Dynamic single row QWERTY key layout	CLIFT, GRAHAM
10610779	6998816	150	06/30/2003	SYSTEM AND METHOD FOR REDUCING EXTERNAL BATTERY CAPACITY REQUIREMENT FOR A WIRELESS CARD	CLIFT, GRAHAM ANTHONY

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